

Figure 1

(Prior art)

The diagram illustrates a system 100 with four nodes (Node 0, Node 1, Node 2, Node 3) connected in a mesh topology. Each node contains a Service processor, NVRAM, and BIOS. The nodes are interconnected via a central bus 106-0 and peripheral buses 106-1, 106-2, and 106-3. External components include RXE 118a and 118b, and a Remote Manager 120.

Node 0 (108) contains a Service processor (110), NVRAM (112), and BIOS (114). Node 1 (102-1) contains a Service processor (7), NVRAM, and BIOS. Node 2 (102-2) contains a Service processor (3), NVRAM, and BIOS. Node 3 (102-3) contains a Service processor (4), NVRAM, and BIOS. The nodes are connected via a central bus 106-0 and peripheral buses 106-1, 106-2, and 106-3. External components include RXE 118a and 118b, and a Remote Manager 120.

Figure 2

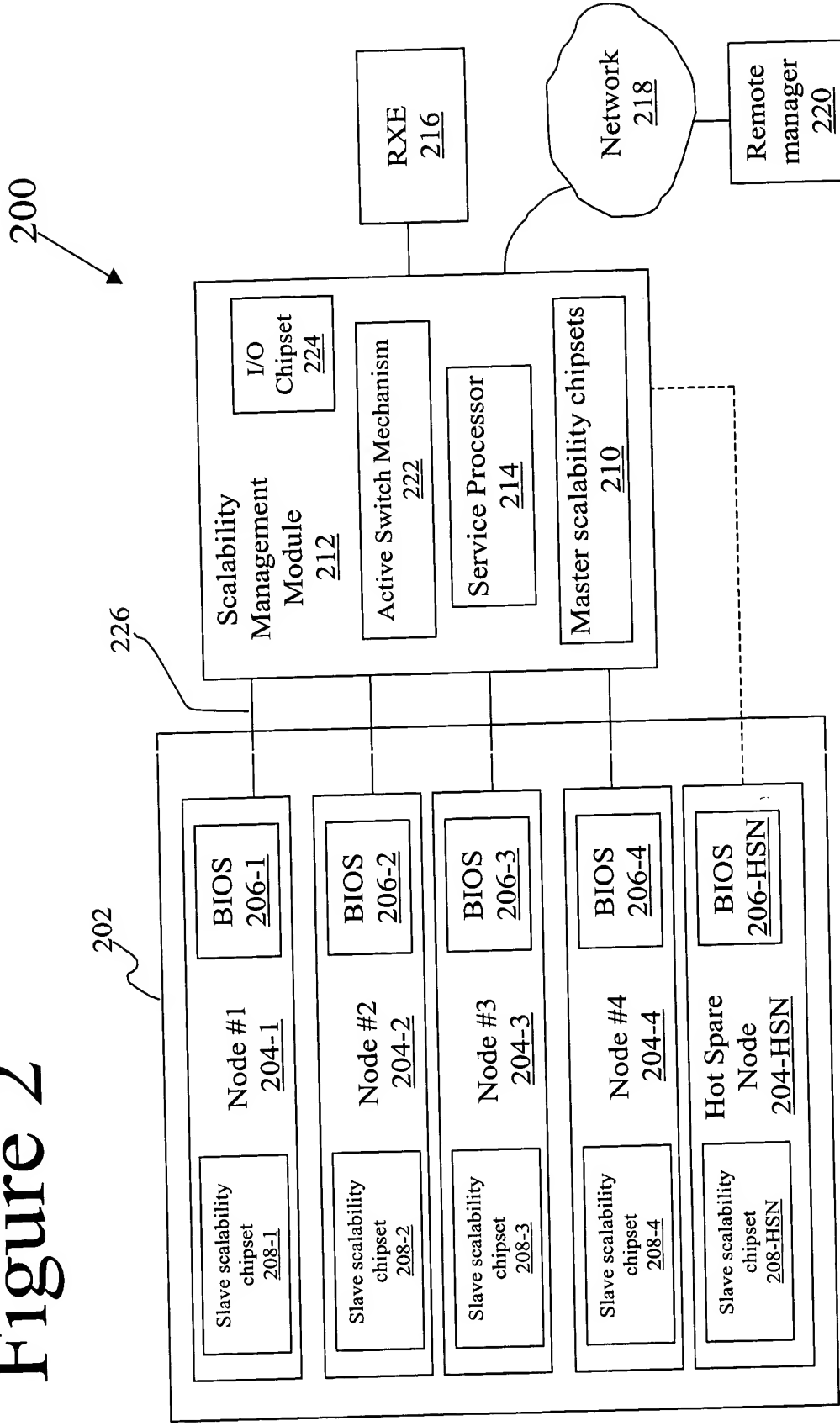


Figure 3

